## *Best Paper Award*

- S. Ahyoune, J. Sieiro, T. Carrasco, M. N. Vidal, J. M. López-Villegas, E. Roca and F. V. Fernández.
  **Extending the Frequency Range of Quasi-Static Electromagnetic Solvers**

## *Runner-up Paper Award*

- J. M. Martinez, R. Rodríguez, M. Nafria, G. Torrens, S. Bota, J. Segura, F. Moll and A. Rubio.
  **Statistical characterization and modeling of random telegraph noise effects in 65nm SRAMs cells**

- M. Kreißig and F. Ellinger.
  **Extended analysis of idealized class-E operation with finite DC feed inductance and preservation of zero volt switching at variable load**

## *Best Paper Award Nominees* [(*)]

- R. De Rose, G. Carangelo, M. Lanuzza, F. Crupi, G. Finocchio and M. Carpentieri.
  **Impact of voltage scaling on STT-MRAMs through a variability-aware simulation framework**

- J. D. Bastidas-Rodríguez, D. González and C. A. Ramos-Paja.
  **Model of Series-Parallel Photovoltaic Arrays Designed for Parallel Computing**

- L. Giovanni Campana, P. Di Barba, M. E. Mognaschi, F. Dughiero, M. Forzan, P. Sgarbossa, E. Spessot and E. Sieni.
  **Electrical resistance in inhomogeneous samples during electroporation**

- R. Viera, P. Maurine, J. M. Dutertre and R. Possamai Bastos.
  **Importance of IR Drops on the Modeling of Laser-Induced Transient Faults**

- R. Morello, Roberto Di Rienzo, Roberto Roncella, Roberto Saletti and Federico Baronti.
  **Tuning of Moving Window Least Squares-based Algorithm for Online Battery Parameter Estimation**

- F. Passos, E.Roca, R. Castro Lopez, F. V. Fernandez, R. Miguel Martins, N. Lourenço, A. Canelas, R. Povoa and N. Horta.
  **Systematic Design of a Voltage Controlled Oscillator using a Layout-Aware Approach**

- G. Di Capua, N. Femia, K. Stoyca, M. Lodi, A. Oliveri and M. Storace.
  **Ferrite Inductor Models for Switch-Mode Power Supplies Analysis and Design**

- S. Panarello, C. Triolo, F. Garescì, S. Patanè, G. Billè, L. Burian, D. Gazzo, D. Patti, S. Petényi and C. Ribellino.
  **Improving ICs reliability with high speed thermal mapping**

- P. Tertel and L. Hedrich.
  **Real-Time Emulation of Block-Based Analog Circuits on an FPGA**

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## *Competition Winner* **(1000$ prize co-sponsored by IEEE CEDA and IEEE CAS)**

- Axel Hald (and Johannes Seelhorst, Pekka Herzogenrath, Jürgen Scheible and Jens Lienig)
  **A New Method for the Analysis of Movement Dependent Parasitics in Full Custom Designed MEMS Sensor**


## *Competition Runner up Papers* (*)

- Georg Gläser (and Martin Grabmann, Gerrit Kropp and Andreas Fürtig)
  **Automated Generation of System-Level AMS Operating Condition Checks: Your Model's Insurance Policy**

- Pablo Martín-Lloret (and Antonio Toro-Frías, Rafael Castro-López, Javier Martín-Martinez, Elisenda Roca  Moreno, Rosana Rodriguez Martinez, Francisco V Fernández and Montserrat Nafría Maqueda)
  **CASE: A Reliability Simulation Tool for Analog ICs**

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(*) PDF certificates will be sent by mail.